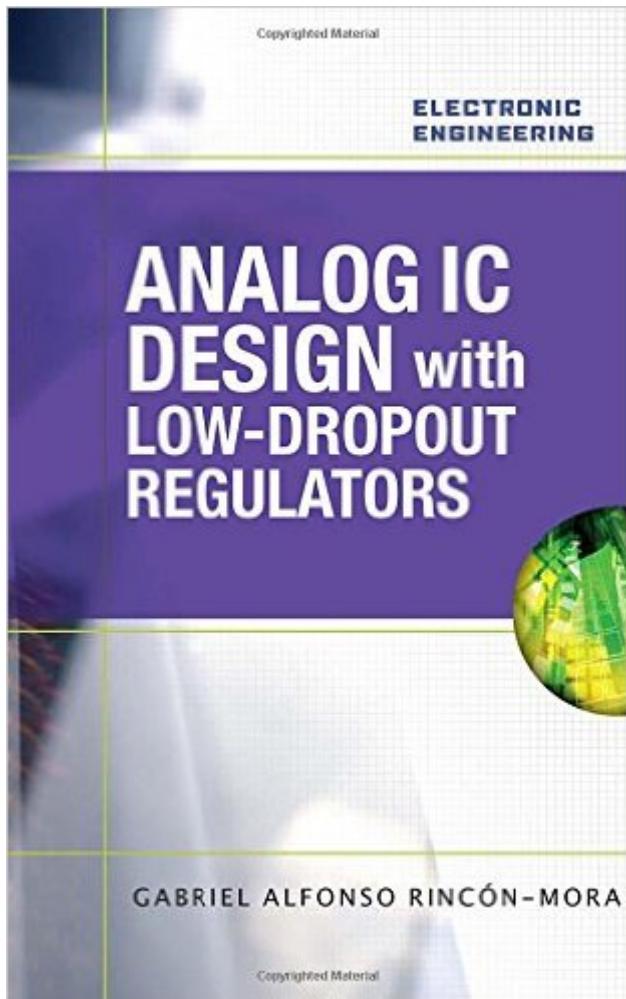


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# Analog IC Design With Low-Dropout Regulators (LDOs) (Electronic Engineering)



## Synopsis

Master Analog Integrated-Circuit DesignDesign, analyze, and build linear low-dropout (LDO) regulator ICs in bipolar, CMOS, and biCMOS semiconductor process technologies. This authoritative guide offers a unique emphasis on embedded LDO design. Through intuitive explanations and detailed illustrations, the book shows how you can put these theories to work creating analog ICs for the latest portable, battery-powered devices.

Analog IC Design with Low-Dropout Regulators details the entire product development cycle—from defining objectives and selecting components to blueprinting, assembling, and fine-tuning performance. Work with semiconductors, employ negative feedback, handle fluctuating loads, and embed regulators in ICs. You will also learn how to build prototypes, perform tests, and integrate system-on-chip (SoC) functionality. Discover how to:

- Design, test, and assemble BJT-, MOSFET-, and JFET-based linear regulators
- Use current mirrors, buffers, amplifiers, and differential pairs
- Integrate feedback loops, negative feedback, and control limits
- Maintain an independent, stable, noise-free, and predictable output voltage
- Compensate for low input current and wide voltage swings
- Optimize accuracy, efficiency, battery life, and integrity
- Implement overcurrent protection and thermal-shutdown features
- Establish power and operating limits using characterization techniques

## Book Information

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## Customer Reviews

The English and the terminology are not conventional for Engineering, which make me confuse and

take a long time to read it. Some of the equations are either. First 4 chapter, which is half of it, is only the review of basic analog circuit. Last 4 chapters are somewhat helpful. I would rather read other good papers for LDO.

From page ii (about the author): "...worked for Texas Instruments from 1994 to 2003,.....5 books and 1 book chapter, 26 patents, 100+ scientific publications,.....26 commercial power management chip designs.....adjunct professor at Georgia Tech in 1999,...faculty member in 2001..." Should I say anything more to recommend this book to the people in the industry?

I have been working as an IC designer specializing in power electronics for well over a decade. Over the course of my career I have been exposed to many text books. There are very few I would consider adding to my collection. As a point of reference I consider "Gray & Meyer" to be the gold standard of text books covering analog IC design. I only ever feel compelled to write a review if the object of the review is either exceedingly excellent or exceedingly poor. In the case of this text I believe it is exceedingly excellent. It is an advanced text and therefore I would not recommend it to an undergraduate engineering student. I have found the text to be both lucid and entertaining. The information is presented in a logical order and is germane to IC design in general. As implied by the title, this text book does not cover a wide array of topics like Gray & Meyer does, but the topics it does cover it covers very well. The chapters are titled as follows: 1: System Considerations 2: Microelectronic Devices 3: Analog Building Blocks 4: Negative Feedback 5: AC Design 6: IC Design 7: System Design 8: IC Protection and Characterization

This book has so many flaws and typos that I got frustrated. I would be glad if the author of this book reads this review and take back his book and refund my money.. Looks like the two 5 star review ratings are given by author or his friends!

Good seller - great package - item as described

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